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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of
Bhanu KAPOOR, et al.

Docket No: Q83911

Appln. No.: 10/711,971

Group Art Unit: 2825

Confirmation No.: 5970

Examiner: DO, THUAN V

Filed: October 15, 2004

For: A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR AUTOMATIC
INSERTION AND CORRECTNESS VERIFICATION OF LEVEL SHIFTERS IN
INTERGRATED CIRCUITS WITH MULTIPLE VOLTAGE DOMAINS.

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

ATTN: MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

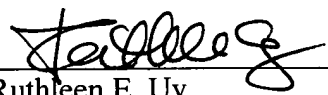
Sir:

REMARKS

Applicant offers the following comments in response to the Examiner's Statement of Reasons for Allowance set forth on page 2 of the Notice of Allowability dated August 1, 2006. Applicant notes that independent reasons, other than those indicated by the Examiner, exist for the allowability of claims 1-36. Further, Applicant submits that the claims are allowable because of the combination of the claimed features, and not based on the Examiner's Reasons for Allowance. Moreover, the claims should be governed by the actual claim language and not the Examiner's reasons for allowance.

Respectfully submitted,

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860



Ruthleen E. Uy
Registration No. 51,361

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 23, 2006